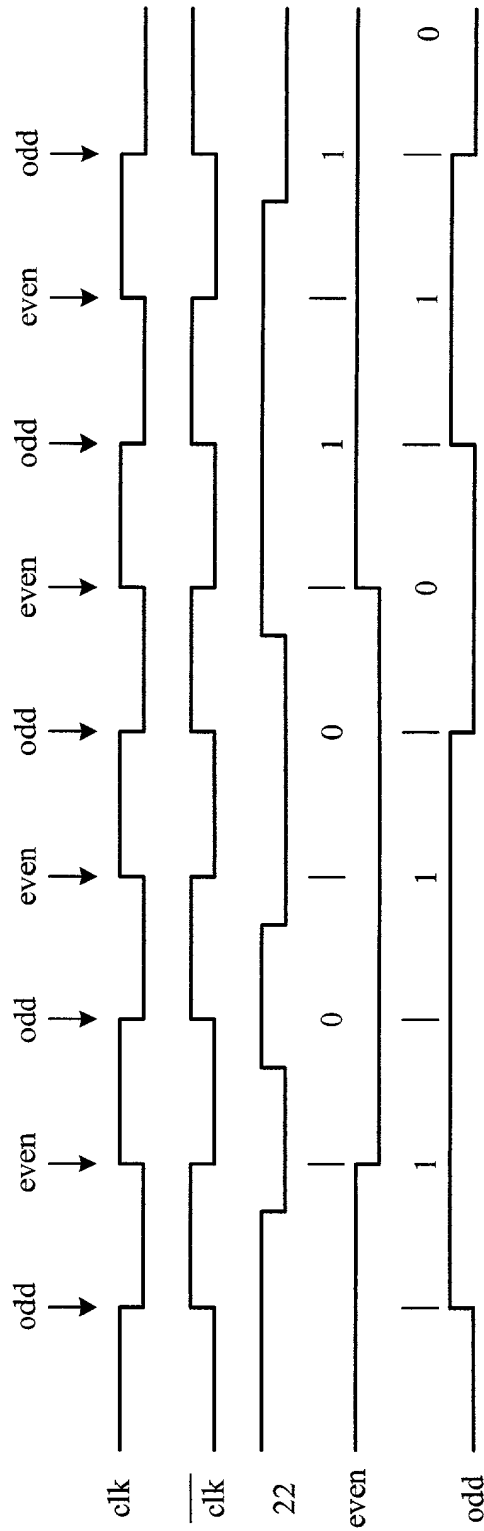
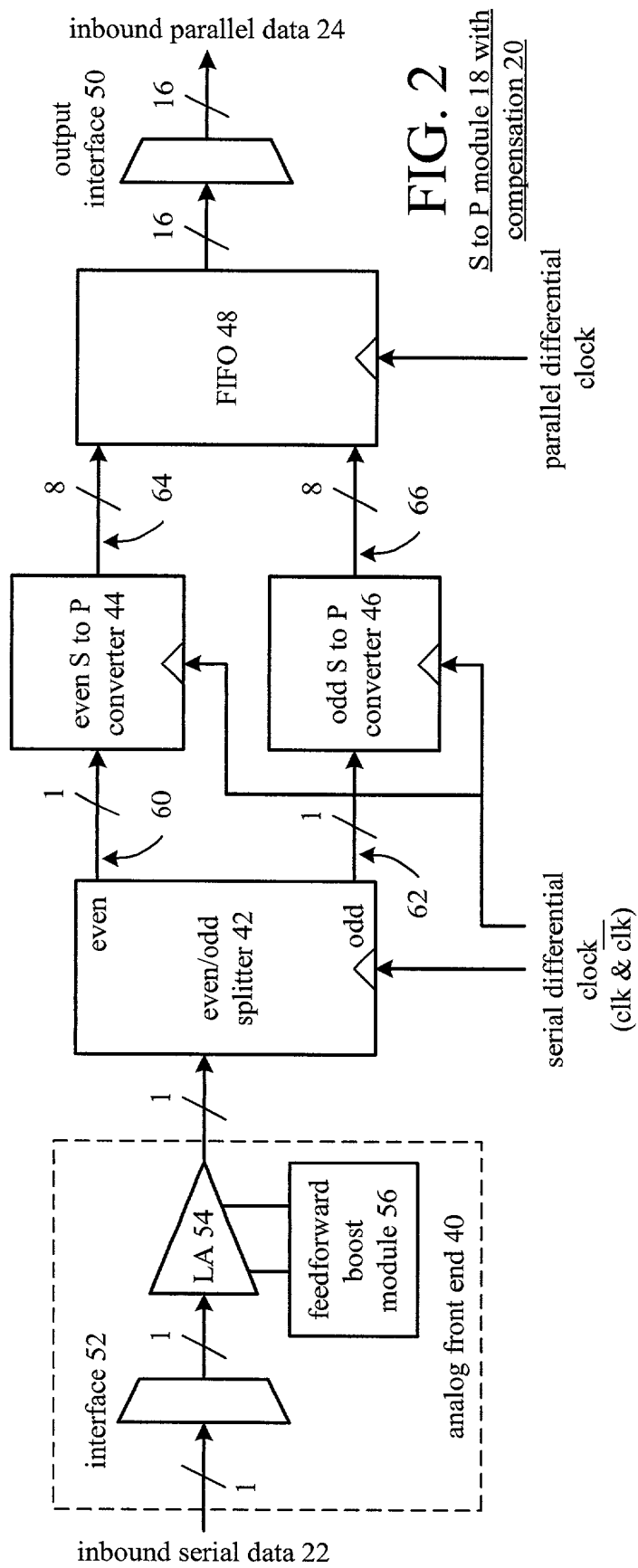


FIG. 1
transceiver 10



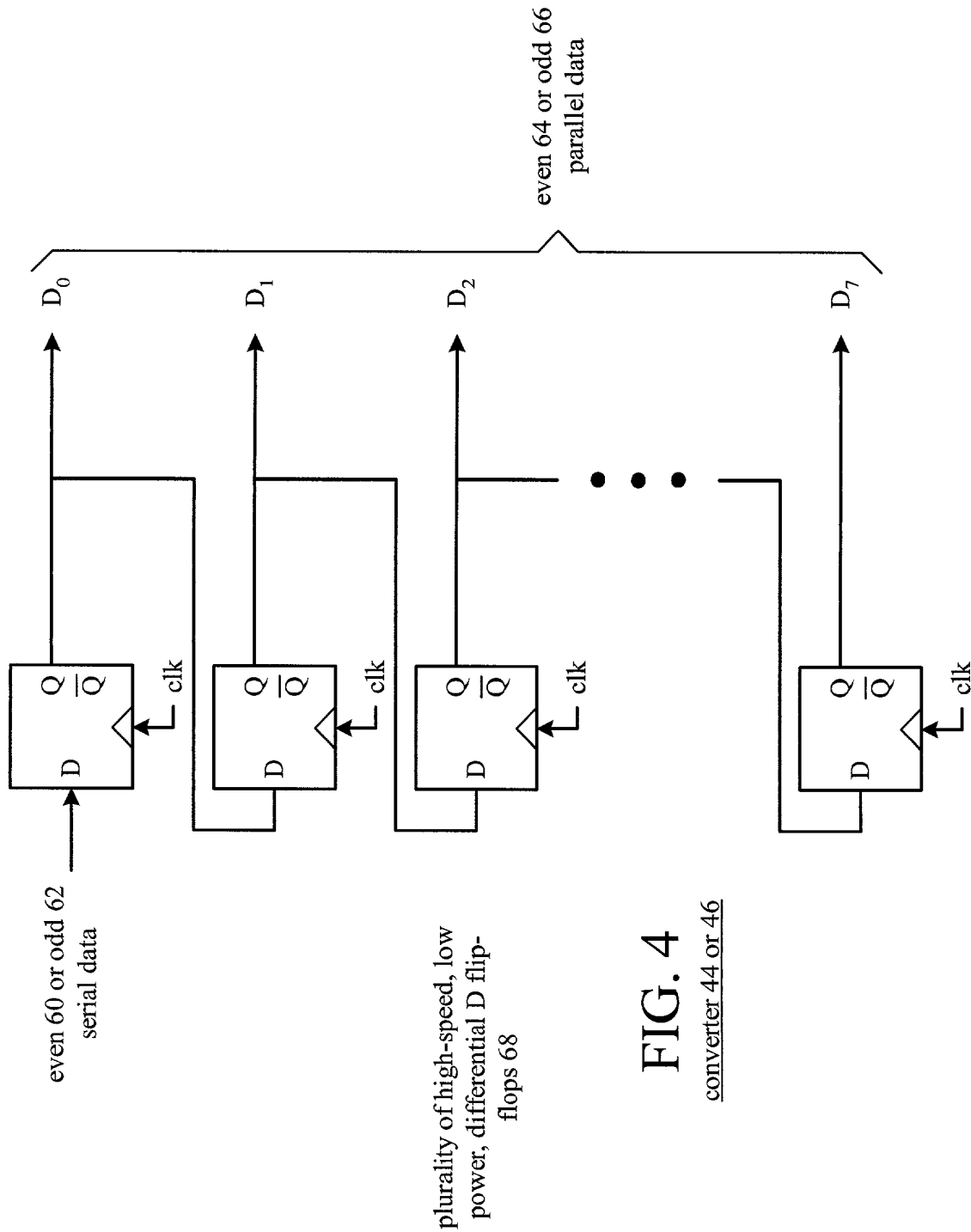


FIG. 4

converter 44 or 46

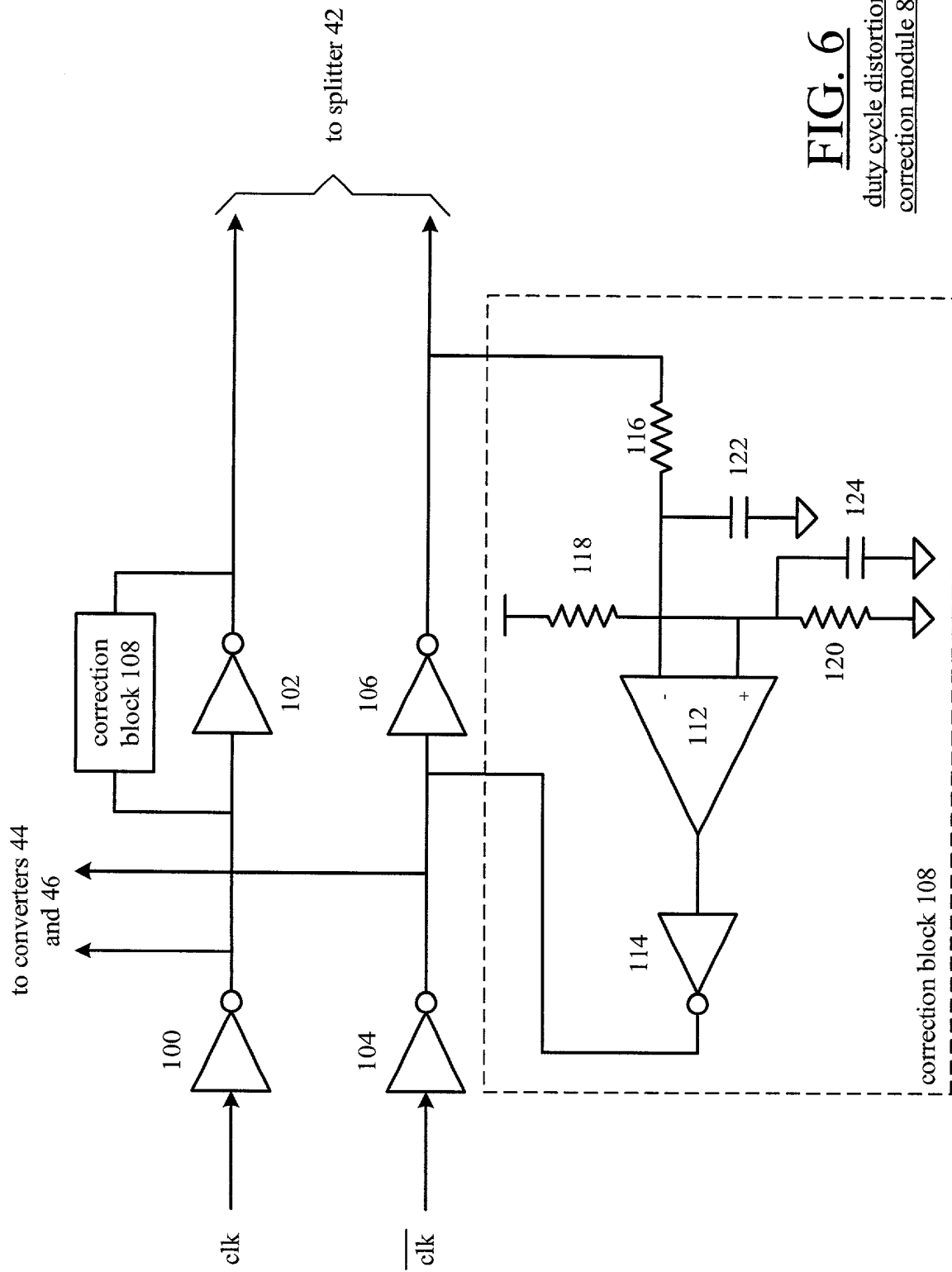


FIG. 6
duty cycle distortion
correction module 88

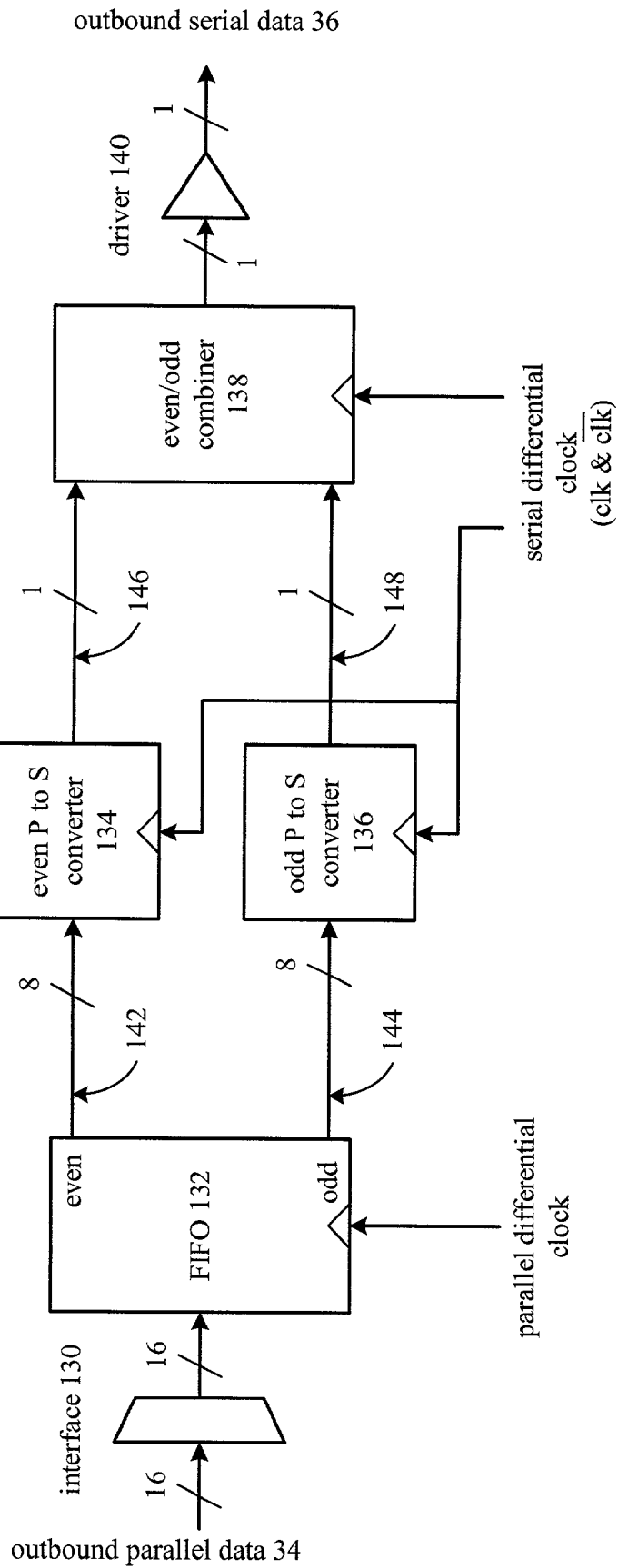


FIG. 7

P to S module 30 with
compensation 32

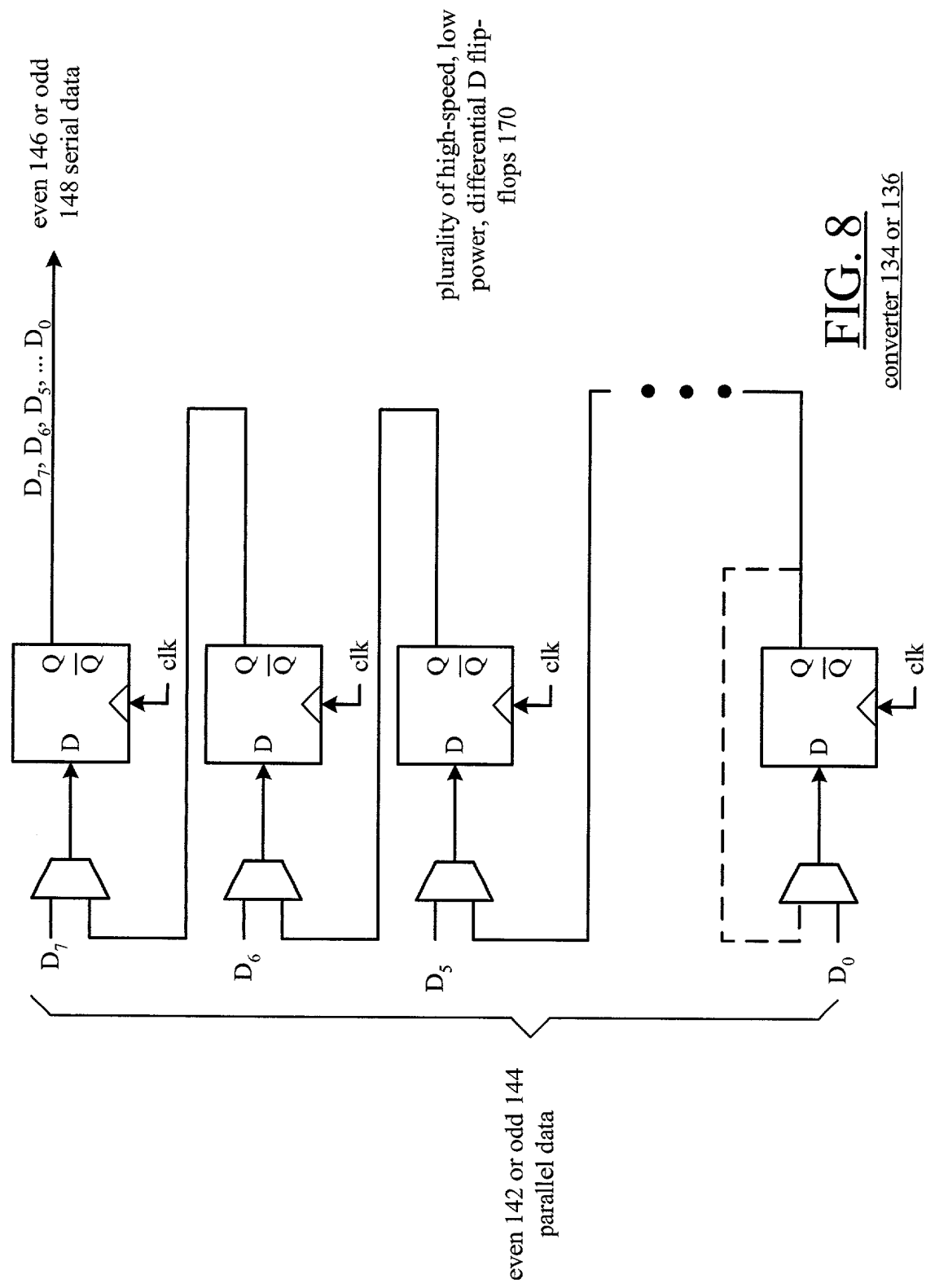


FIG. 8
converter 134 or 136

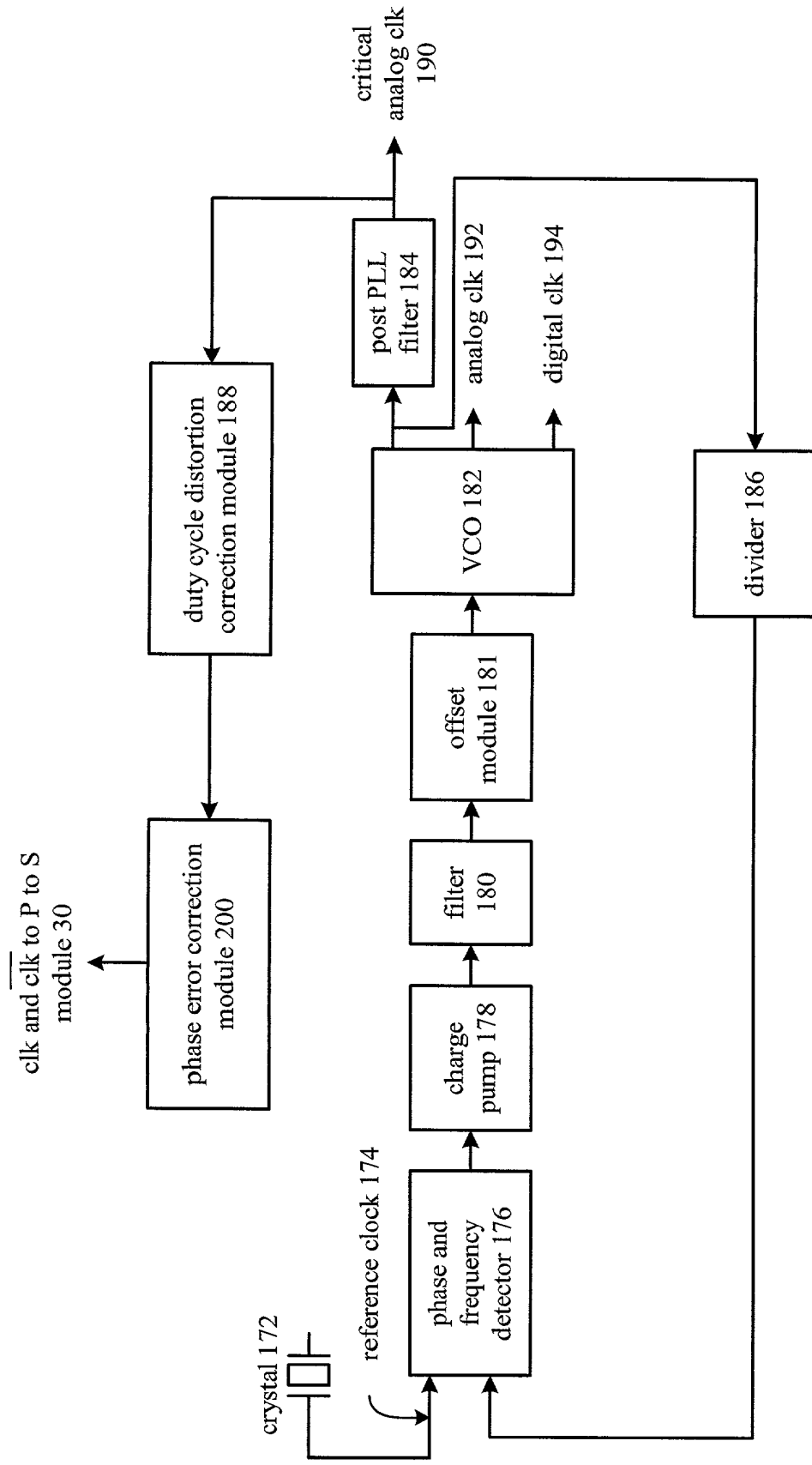


FIG. 9

Tx clocking circuit 28 with compensation 32

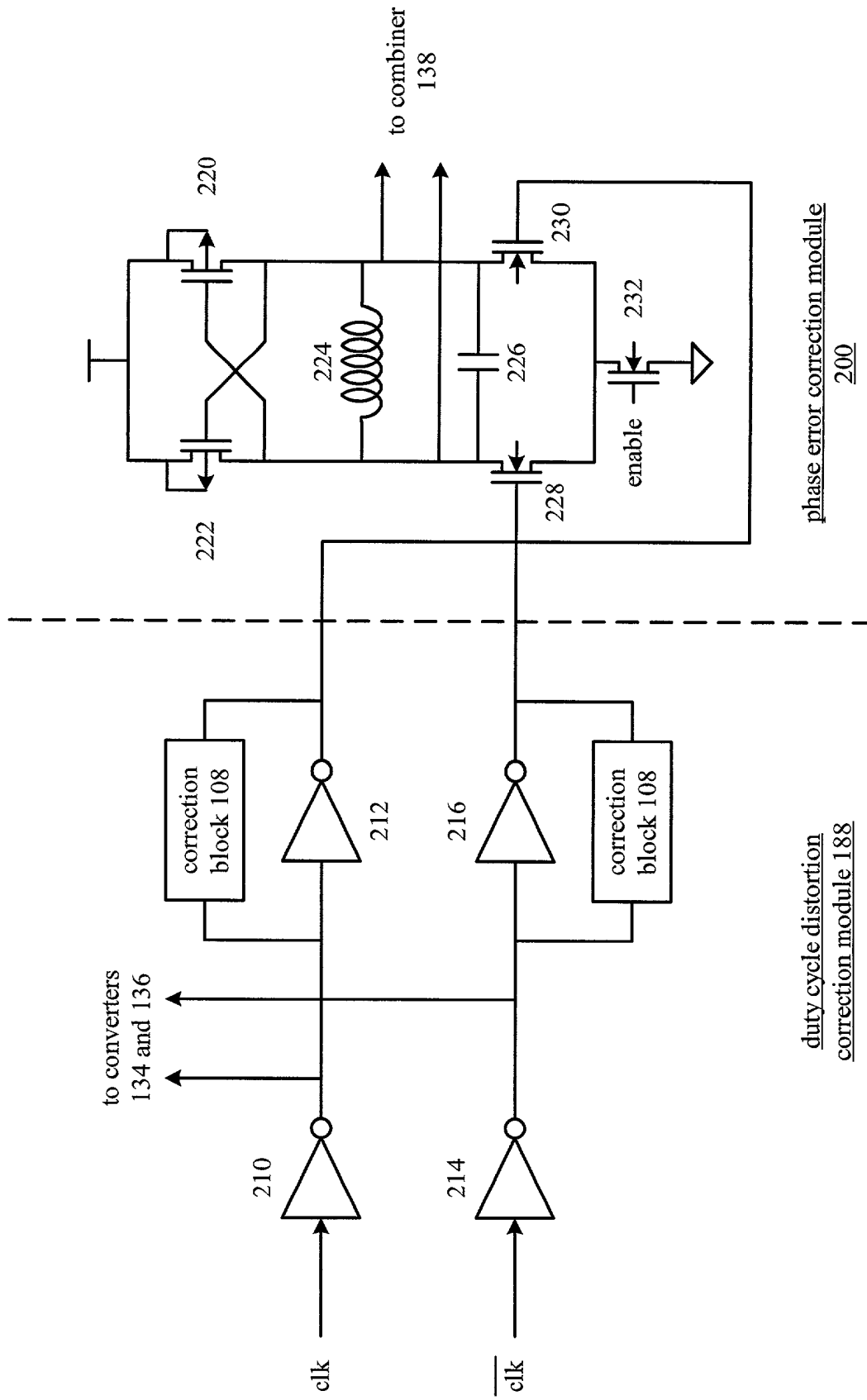


FIG. 10

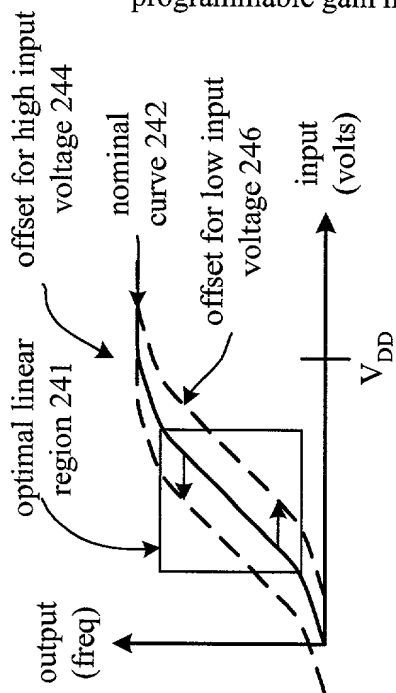


FIG. 11
VCO curve

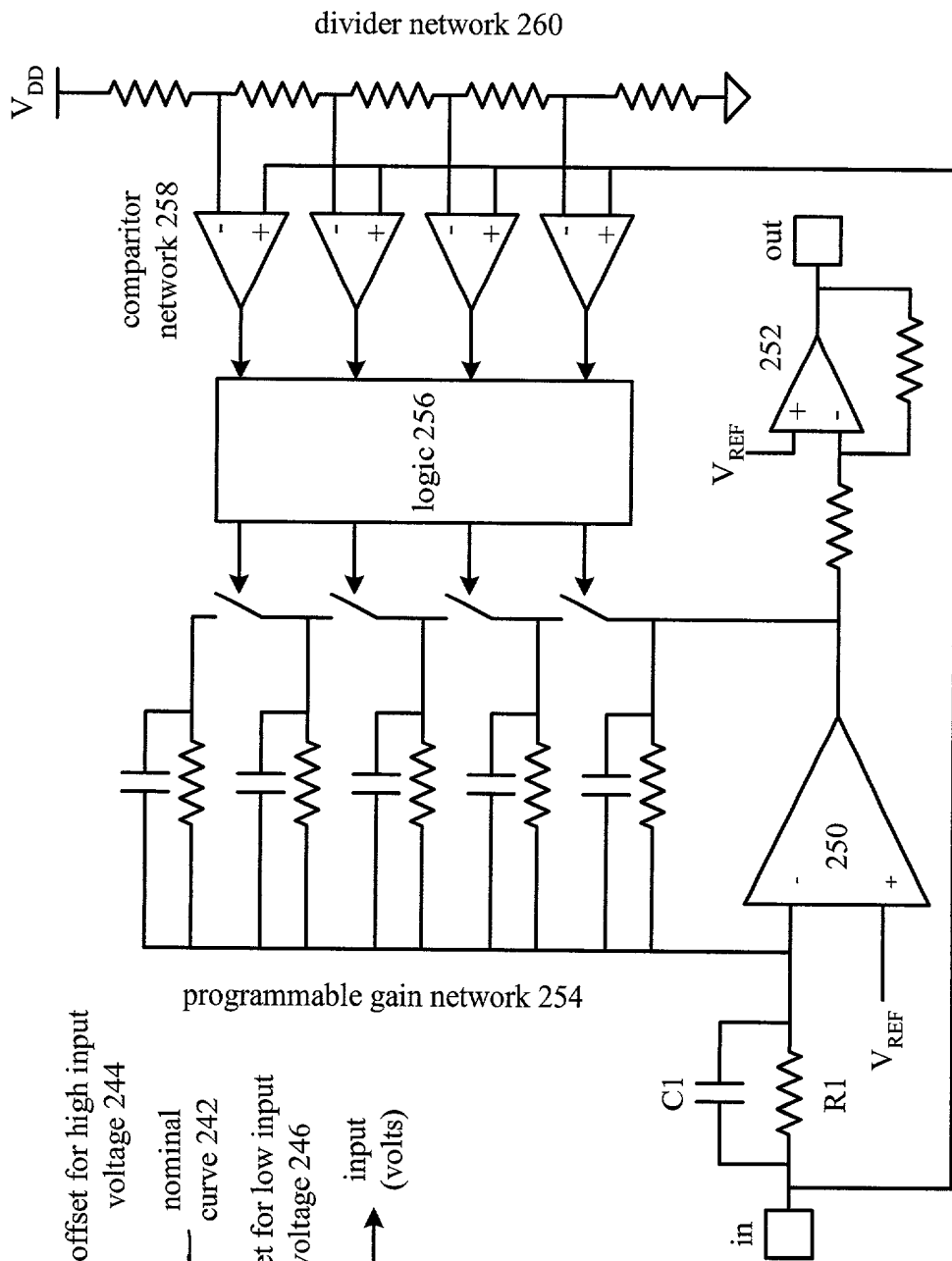


FIG. 12
offset module 181

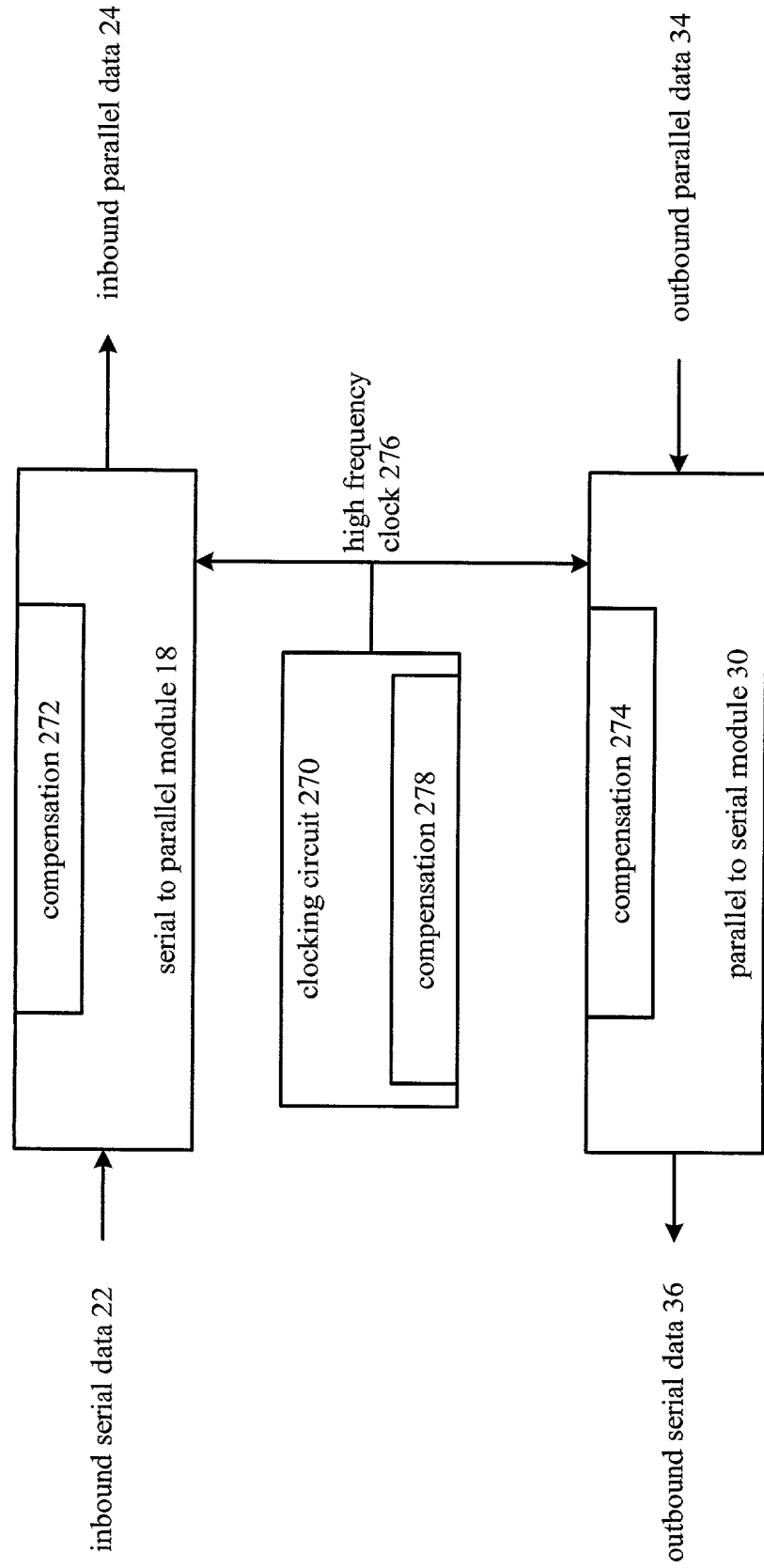


FIG. 13

transceiver 275